

IN THE CLAIMS

Please amend the claims as follows:

Claim 1 (Currently Amended): A drive method for an EL display panel that has a display screen in which pixels are formed in a matrix state, the EL display panel comprising:
EL elements arranged in a matrix;
driver transistors which supply current to be passed through the EL elements;
a first condenser having a first terminal and a second terminal;
first switching elements placed in current paths of the EL elements; [[and]]
a switching transistor which is connected between a gate terminal and another terminal of said driver transistor; and
a gate driver circuit which turns on and off the first switching elements for control;
wherein:
said first terminal of the condenser is connected to said gate terminal of the driver transistor,
said second terminal of the first condenser is connected to an electrode to which a predetermined voltage is impressed,
said switching transistor has a multi-gate structure,
the gate driver circuit generates a plurality of stripe non-display areas on said [[a]] display screen of the EL display panel by controlling the first switching elements in an off-state two or more times during one frame period,
the gate driver circuit moves the plurality of stripe non-display areas in a scanning direction of the gate driver circuit at a cycle of one frame period, and
an operation for retaining an image signal applied to each pixel is executed only once during the one frame period.

Claim 2 (Currently Amended): A drive method for an EL display panel that has a display screen in which pixels are formed in a matrix state, the EL display panel comprising:

EL elements arranged in a matrix;

driver transistors which supply current to be passed through the EL elements; [[and]]

a first condenser having a first terminal and a second terminal;

first switching elements placed in current paths of the EL elements;

a switching transistor which is connected between a gate terminal and another terminal of said driver transistor; and

a gate driver circuit which selects pixel [[row]] rows of the EL display panel in sequence;

wherein,

said first terminal of the condenser is connected to said gate terminal of the driver transistor,

said second terminal of the condenser is connected to an electrode to which a predetermined voltage is impressed,

said switching transistor has a multi-gate structure,

a start pulse to be input into the gate driver circuit is controlled to turn on and off said first switching element,

a plurality of stripe non-display areas on [[a]] said display screen of the EL display panel are generated and the plurality of stripe non-display areas are moved in a scanning direction of the gate driver circuit at a cycle of one frame period; and

an operation for retaining an image signal applied to each pixel is executed only once during the one frame period.

Claim 3 (Canceled).

Claim 4 (Previously Presented): The drive method for the EL display panel according to claim 1,

wherein the first switching elements are turned off periodically during one frame period.

Claim 5 (Canceled).

Claim 6 (Currently Amended): The drive method for the EL display panel according to claim [[5]] 1, wherein the ~~first and second gate driver circuits are~~ circuit is formed in a same process as the driver transistors ~~and the source driver circuit is made of a semiconductor chip.~~

Claims 7-9 (Canceled).

Claim 10 (Currently Amended): The drive method for the EL display panel according to claim [[5]] 1, wherein the gate driver circuit is constructed of p-channel transistors.

Claims 11-15 (Canceled).

Claim 16 (Previously Presented): The drive method for the EL display panel according to claim 1, wherein the driver transistors are P-channel transistors.

Claim 17 (Previously Presented): The drive method for the EL display panel according to claim 2, wherein the driver transistors are P-channel transistors.

Claim 18 (Previously Presented): The drive method for the EL display panel according to claim 1, wherein brightness of the display screen is varied or controlled by varying proportion of the non-display area to a display area of the display screen.

Claim 19 (Previously Presented): The drive method for the EL display panel according to claim 2, wherein brightness of the display screen is varied or controlled by varying proportion of the non-display area to a display area of the display screen.

Claim 20 (Canceled).

Claim 21 (Currently Amended): The drive method for the EL display panel according to claim [[5]] 2, wherein the ~~first~~ gate driver circuit is formed with P-channel transistors.

Claim 22 (New): An EL display panel that has a display screen in which pixels are formed in matrix state, comprising:

a gate driver circuit which selects a line of the pixels of said display screen; and
a source driver circuit which supplies an image signal that is to be impressed to said pixels;

wherein,

each said pixel has an EL element, a driver transistor which supplies current to the EL element, a first switching element and a second switching element,
said gate driver circuit turns on said second switching element to impress a first voltage to a selected line of the pixels,

said gate driver circuit turns on said first switching element to impress said image signal to said line of the pixels,

 said image signal is impressed to said line of the pixels after said first voltage is impressed to said line of the pixels,

 said line of the pixels to which said first voltage was impressed becomes a non-display area until said image signal is impressed to said line of the pixels, and

 said non-display area is moved in a scanning direction of said gate driver circuit at a cycle of frame period.

Claim 23 (New): The EL display panel according to claim 22, further comprising:

 a condenser that is formed in said pixel, wherein

 said condenser has a first terminal and a second terminal,

 a switching element comprising a switching transistor,

 said first terminal of said condenser is connected with a gate terminal of said switching transistor, and said second terminal of said condenser is connected with a gate terminal of said driver transistor.

Claim 24 (New): The EL display panel according to claim 22, wherein

 after said second switching element of the pixel is turned on and said first voltage is impressed to said pixel, and

 after a plurality of horizontal scanning periods, said first switching element of the pixel is turned on to impress said image signal to said pixel.

Claim 25 (New): The EL display panel according to claim 22, further comprising:

 a selection circuit formed on an output side of said source driver circuit, wherein

said source driver circuit outputs a signal from a signal output terminal,
 said selection circuit has a plurality of groups, each one of said groups including one
input terminal and a plurality of selection output terminals that can be connected with said
one input terminal,

 each one of said input terminals is connected with said signal output terminal of said
source driver circuit, and each one of said selection output terminals is connected with a
source signal line for transmitting the image signal to said pixel, and

 said selection circuit selects one or more selection output terminal from said a
plurality of selection output terminals and impresses said signal impressed to said input
terminal of said selection circuit to said source signal line connected with said selected
selection output terminal.

Claim 26 (New): The EL display panel according to claim 22, wherein
 said each pixel further has a third switching element which short-circuits a gate
terminal and the other terminal of said driver transistor, and
 said third switching element is a transistor having a multi-gate structure.

Claim 27 (New): The EL display panel according to claim 22, further comprising:
 a detection unit which detects a brightness of extraneous light.

Claim 28 (New): The EL display panel according to claim 22, wherein a control
signal for said gate driver circuit is supplied from said source driver circuit.

Claim 29 (New): The EL display panel according to claim 22, wherein a said first
switching element is a transistor having a multi-gate structure.

Claim 30 (New): The EL display panel according to claim 22, wherein
said each pixel includes a first color pixel and a second color pixel,
a size of said first color pixel is different from a size of said second color pixel.